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G Alverson, S Kahan, R Korry, C McCann, B Smith - LECTURE NOTES IN COMPUTER SCIENCE, 1995 - Springer

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[PDF] • Scheduling on the Tera MTA

GASKR Korry, CMCB Smith - reference.kfupm.edu.sa

... The Tera MTA architecture[1] implements a physically shared ... architectural features are especially useful for scheduling ... associated with each protection domain. ...

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Processor Management in the Tera MTA Computer System

G Alverson, S Kahan, R Korry - 1993 - Storming Media

... detailed issues and policies involved in processor scheduling. ... 8. 2. Architecture

The Tera MTA architecture, described ... A protection domain has a memory map and ...

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R Korry, C McCann, B Smith - 1994 - Storming Media

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Tera Hardware Software Cooperation

G Alverson, P Briggs, S Coatney, S Kahan, R Korry - Supercomputing, ACM/IEEE 1997 Conference, 1997 - ieeexplore.ieee.org

... Domains The MTA has sixteen protection domains per processor. This has allowed an innovative type of scheduling [1]. The OS dedicates one protection domain to ...

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Parallel Job Scheduling: Issues and Approaches

DG Feitelson, L Rudolph - LECTURE NOTES IN COMPUTER SCIENCE, 1995 - Springer

... repartitioning, which include switching processors from one protection domain to another ... the operating system and the application's internal scheduler, so as to ...

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[PDF] • A near-memory processor for vector, streaming and bit manipulation workloads

M Wei, M Smir, J Torrellas, RB Tremaine - Watson Conference on Interaction between Architecture, ..., 2005 - wing.cs.uiuc.edu

... the allocation of space in these buffers and the scheduling of producers ... to the one used by the HEP [6] and MTA machines[4 ... Processor (NMP) Core Near Memory DRAM ...

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[PDF] • Tera Hardware-Software Cooperation

GAPBS Coatney, S Kahan, R Korry - cray.com

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[PDF] • Final report for the Tera Computer TTI CRADA

GS Davidson, C Pavlakos, C Silva - 1997 - infoserve.sandia.gov

... The Tera MTA supports HIPPI-PH, -FP, -LE, and ... run-time parallelism increases, additional protection domains can ... into groups and apply scheduling operations to ...

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Method and system for memory allocation in a multiprocessing environment

BD Koblenz, A. Porterfield, BJ Smith - US Patent 6,353,829, 2002 - freepatentsonline.com
... Mook Moon, "Split-Path Enhanced Pipeline **Scheduling** for Loops ... Each **MTA** processor
supports as many as 16 ... stream is assigned to a **protection domain**, but which ...
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Key authors: **G Alverson - D Feitelson - R Korry - L Rudolph - S Kahan**

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